

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S165	27	fpu adj2 (counter\$1 timer\$1)	USPAT	OR	OFF	2005/05/31 16:09
S166	393	latency near2 (counter\$1 timer\$1)	USPAT	OR	OFF	2005/05/31 16:28
S167	6	S166 with instruction near2 (issu\$3 disapatch\$2 schedul\$3)	USPAT	OR	OFF	2005/05/31 16:50
S168	7	S166 same instruction near2 (issu\$3 disapatch\$2 schedul\$3)	USPAT	OR	OFF	2005/05/31 17:22
S169	12	S166 same clock near2 (gat\$3 enabl\$3 disabl\$3)	USPAT	OR	OFF	2005/05/31 17:17
S170	1063	latenc\$3 near4 instruction\$1	USPAT	OR	OFF	2005/05/31 17:14
S171	4	S170 same clock near2 (gat\$3 enabl\$3 disabl\$3)	USPAT	OR	OFF	2005/05/31 16:49
S172	28	S170 same gat\$3	USPAT	OR	OFF	2005/05/31 16:49
S173	4	S170 with gat\$3	USPAT	OR	OFF	2005/05/31 16:49
S174	452	(counter\$1 timer\$1) with instruction near2 (issu\$3 disapatch\$2 schedul\$3)	USPAT	OR	OFF	2005/05/31 16:50
S175	54	S174.clm.	USPAT	OR	OFF	2005/05/31 16:56
S176	12	S174 same latenc\$3	USPAT	OR	OFF	2005/05/31 16:56
S177	562	latenc\$3 near4 instruction\$1	US-PGPUB	OR	OFF	2005/05/31 17:13
S178	27	S177 same gat\$3	US-PGPUB	OR	OFF	2005/05/31 16:59
S179	2	S177 same clock near2 (enabl\$3 disabl\$3)	US-PGPUB	OR	OFF	2005/05/31 16:59
S180	95	latenc\$3 with instruction\$1	EPO; JPO; IBM_TDB	OR	OFF	2005/05/31 17:13
S181	1	S180 same gat\$3	EPO; JPO; IBM_TDB	OR	OFF	2005/05/31 17:02
S182	0	S180 same clock near2 (enabl\$3 disabl\$3)	EPO; JPO; IBM_TDB	OR	OFF	2005/05/31 17:02
S183	0	("6779122").URPN.	USPAT	OR	OFF	2005/05/31 17:02
S184	4	("5414863"   "5958044"   "6100752"   "6128747").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/05/31 17:02
S185	6	S180 same power\$4	EPO; JPO; IBM_TDB	OR	OFF	2005/05/31 17:13
S186	16	S177 with power\$4	US-PGPUB	OR	OFF	2005/05/31 17:14
S187	22	S170 with power\$4	USPAT	OR	OFF	2005/05/31 17:14
S188	241	instruction near6 clock near2 (gat\$3 enabl\$3 disabl\$3)	USPAT	OR	OFF	2005/05/31 17:17
S189	349	instruction with clock near2 (gat\$3 enabl\$3 disabl\$3)	USPAT	OR	OFF	2005/05/31 17:18
S190	45	S189.clm.	USPAT	OR	OFF	2005/05/31 17:20
S191	63	S189 same (counter\$1 timer\$1 timeout\$1)	USPAT	OR	OFF	2005/05/31 17:21

S19 3	13	S174.ab.	USPAT	OR	OFF	2005/05/31 17:28
S19 4	474	latency near2 instruction	USPAT	OR	OFF	2005/05/31 17:29
S19 5	324	latenc\$3 near2 instructions	USPAT	OR	OFF	2005/05/31 17:29
S19 6	10	(counter\$1 timer\$1) near6 (S194 S195)	USPAT	OR	OFF	2005/05/31 17:29
S19 7	3963	fpu "floating point" adj2 unit\$1	USPAT	OR	OFF	2005/06/01 11:14
S19 8	106	S197 with (counter\$1 timer\$1)	USPAT	OR	OFF	2005/06/01 11:15
S19 9	1	S198.clm.	USPAT	OR	OFF	2005/06/01 11:15
S20 0	0	("6826704").URPN.	USPAT	OR	OFF	2005/06/01 11:34
S20 1	505	(wait\$3 delay\$3) with instruction near4 (dispatch\$3 issue schedul\$3)	USPAT	OR	OFF	2005/06/01 11:37
S20 2	17	S201.ab.	USPAT	OR	OFF	2005/06/01 11:35
S20 3	125	(activ\$3 inactiv\$3) with instruction near4 (dispatch\$3 issue schedul\$3)	USPAT	OR	OFF	2005/06/01 11:37
S20 4	16	S203.clm.	USPAT	OR	OFF	2005/06/01 11:42
S20 5	103	latency with clock near2 (gat\$3 enabl\$3 disabl\$3)	USPAT	OR	OFF	2005/06/01 11:51
S20 6	6	instruction near4 (period interval) with clock near2 (gat\$3 enabl\$3 disabl\$3)	USPAT	OR	OFF	2005/06/01 11:46
S20 7	8	S205.clm.	USPAT	OR	OFF	2005/06/01 11:47
S20 8	302	gat\$3 near4 (execution functional) adj (unit\$1 block\$1)	USPAT	OR	OFF	2005/06/01 11:47
S20 9	1	S208 with latency	USPAT	OR	OFF	2005/06/01 11:47
S21 0	4	S208 with (period interval)	USPAT	OR	OFF	2005/06/01 11:49
S21 1	12	S208 with (counter\$1 timer\$1 timeout)	USPAT	OR	OFF	2005/06/01 11:49
S21 2	5695	(counter\$1 timer\$1) with clock near2 (gat\$3 enabl\$3 disabl\$3)	USPAT	OR	OFF	2005/06/01 11:55
S21 3	122	S212.ab.	USPAT	OR	OFF	2005/06/01 11:52
S21 4	31	S212 with instruction	USPAT	OR	OFF	2005/06/01 11:54
S21 5	0	S212 same instruction with (dispatch\$3 issue schedul\$3)	USPAT	OR	OFF	2005/06/01 11:55
S21 6	3040	(counter\$1 timer\$1) near3 clock near2 (gat\$3 enabl\$3 disabl\$3)	USPAT	OR	OFF	2005/06/01 11:55
S21 7	856	(period interval) near3 clock near2 (gat\$3 enabl\$3 disabl\$3)	USPAT	OR	OFF	2005/06/01 11:55
S21 8	14	S217.ab.	USPAT	OR	OFF	2005/06/01 11:56